

# Call for Designs

University LSI Design Contest

ASP-DAC 2018

<http://www.aspdac.com/>

January 22-25, 2018

Jeju Island, Korea 



## Aims of the Contest:

As a unique feature of ASP-DAC 2018, the University LSI Design Contest will be held. The aim of the contest is to encourage education and research on VLSI design at universities and other educational organizations. We solicit designs that fit in one or more of the following categories:

- (1) Designs implemented on chips in universities or other educational organizations during the last two years;
- (2) Designs that report measurement results from actual implementations;
- (3) Innovative design prototypes.

Excellent designs will be selected and honored by providing the opportunities for presentation in a special session at the conference. Award(s) will be given to a few outstanding designs, selected from those presented at the conference.

## Areas of Design:

Application areas or types of the original LSI circuit designs include (but are not limited to):

- (1) Analog, RF and Mixed-Signal Circuits, (2) Digital Signal Processors, (3) Microprocessors, (4) Custom ASIC.

Methods or technologies used for implementation include:

- (a) Custom ASIC and Cell-Based LSIs, (b) Gate Arrays, (c) FPGA/PLDs.

## Submission of Design Descriptions:

A camera-ready summary is requested to be prepared within 2 pages including figures, tables, and references. It is strongly recommended that measured experimental results and chip micrographs are included in the summary. Please do not submit the same paper as a regular paper.

Specification of the submission format will be available at <http://www.aspdac.com/>

<b>Deadline for summary:</b>	5 PM AOE (Anywhere on earth) July 7 (Fri), 2017
<b>Notification of acceptance:</b>	Sep. 11 (Mon), 2017
<b>Deadline for camera-ready:</b>	5 PM AOE (Anywhere on earth) Nov. 6 (Mon), 2017

## Review:

Submitted designs will be reviewed by the Design Contest Committee through a process similar to the review process for the technical papers. The following criteria will be applied in the selection of designs:

- (1) Reliability of design and implementation, (2) Quality of implementation, (3) Performance of the design, (4) Novelty of application, algorithm, and architecture, (5) Others.

Excellent designs will be selected for presentation at a special session of the conference.

## Presentation:

An author of each selected design will be required to make a short presentation at a special session of ASP-DAC 2018.

It is mandatory that at least one co-author per accepted paper registers for the conference at the registration rate of speakers or full-time students. A digest of each design presented will be included in the conference proceedings.

Contact Email: [udc@aspdac18.com](mailto:udc@aspdac18.com)

## ASP-DAC 2018 Chairs

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